Amendments to the Claims:

Please amend the claims as follows.

This listing of claims replaces all prior versions, and listings, of claims in the application.

Listing of claims:

1.   (Original) A filter characteristic measuring method, comprising the steps of:

generating an impulse signal;

applying the impulse signal to a DUT (Device Under Test) having an analog filter through a digital channel; and

measuring a gain of the analog filter in the DUT and a frequency characteristic by using an output of the analog filter.

2.   (Original) The method of claim 1, wherein the analog filter is an equalizing filter.

3.   (Currently Amended) ~~Analog~~An analog filter characteristic measuring method, ~~characterized in that~~ comprising:

applying an impulse signal ~~is applied~~ to an equalizing filter by using a digital channel of an automatic tester, and then obtaining an output response of the equalizing filter ~~is obtained~~ and performing a differential and a fast Fourier transform (FFT) operation on the output response of the equalization filter ~~therefor are performed~~ so as to measure a boosting gain and a frequency response.

4.   (Currently Amended) A system for measuring a characteristic of a filter in a DUT employing an analog filter, said system comprising:

a digital channel for providing an impulse signal without applying a sine wave to the analog filter of the DUT;

a digitizer for receiving an output signal of the analog filter so as to measure the

characteristic of the filter; and

a controller for controlling the digital channel and the digitizer.

5.   (Original) The system of claim 4, wherein the digitizer comprises:

an anti-aliasing filter for antialiasing-filtering an output of the filter;

an analog to digital (A/D) converter for converting a filter output outputted from the anti-aliasing filter into digital data;

a memory for capturing the digital data outputted from the A/D converter at a determined storage region;

a digital signal processing (DSP) for processing in signal the digital data captured at the memory; and

a digital filter for receiving the process signal outputted from the DSP and digitally filtering the process signal.

6.   (Original) The system of claim 4, wherein the analog filter is an equalizing filter.